Correctness Proofs and Complexity Analysis of EventTrack Algorithm

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This is a supplementary material to the paper [2] which presents an algorithm called EventTrack to compute happens-before (HB) relation for event-driven programs such as Android applications. This material presents the correctness proofs and complexity analysis of the EventTrack algorithm.

Assumptions. The correctness proofs as well as complexity analysis presented here make an assumption related to the NO-PRE event-ordering rule presented in Table 1 of [2]. The NO-PRE rule states that for a pair of events $e$ and $e'$ posted to the same event queue, if an operation in the handler of $e$ is found to happen before an operation in the handler of $e'$ then $\text{end}(e) \prec_h \text{deq}(e')$. Let function $hhSrc(z)$ return the set of operations in the trace that happen before an operation $z$ due to some HB rule except purely due to transitive closure. Typically, the set $hhSrc(z)$ can be computed for an operation $z$ in a trace $\tau$ just by inspecting the prefix of $\tau$ up to the index of operation $z$ in the trace. This makes vector clock based HB relation computation complexity linear in the number of operations in the trace. However, the presence of NO-PRE event-ordering rule may cause EventTrack to HB order deq of an event $e'$ w.r.t. end of an event $e$, only after analyzing an operation $y$ such that $\text{target}(y)$ (defined in Table 3 in [2]) is an operation in the handler of $e'$. This in turn may cause the operations in between $\text{deq}(e')$ and $y$ to be reprocessed to reflect the new HB information obtained via $\text{end}(e)$ in their respective vector clock timestamps. In general, computation of vector clock timestamps for operations in the program trace of a multi-threaded event-driven program like an Android application, may not complete in fixed number of passes over the trace.

However we assume that the application of NO-PRE rule does not result in reprocessing of already processed operations in the input execution trace. We make this assumption when giving the correctness proofs and algorithm complexity, as we did not observe such trace reprocessing due to NO-PRE in our experimentation. However, our implementation of EventTrack handled such cases as well. We refer the readers to Section 3 of README.pdf at the root of EventTrack tool repository\(^1\) for more information about our implementation strategy to handle trace reprocessing due to NO-PRE.

1 \(^1\)http://bitbucket.org/iiscseal/eventtrack

1 CORRECTNESS PROOFS

We give correctness proofs for EventTrack algorithm and prove that it indeed computes the happens-before relation for a given execution trace $\tau$ of an event-driven program $P$. These proofs have a framework similar to the correctness proofs of FASTTrack algorithm [1] which performs vector clock based HB computation for multi-threaded programs.

\(^1\)http://bitbucket.org/iiscseal/eventtrack

An operation $z$ belonging to a trace $\tau$ may be indicated as $z \in \tau$. Henceforth, for any operation $z$ in a trace $\tau$ we refer to the analysis state prior to processing $z$ and after processing $z$ by the application of transfer functions, as $\sigma^z = (T^z, G^z)$ and $\hat{\sigma}^z = (\hat{T}^z, \hat{G}^z)$ respectively.

For an operation $z$ we use a notation $T^z$ and $G^z$ to mean the following. If $z$ is of type $\text{tinit}, \text{texit}, \text{join}, \text{trigger}, \text{deq}$ or $\text{end}$ then $T^z = \hat{T}^z$, $G^z = \hat{G}^z$, else $T^z = T^{\text{fork}}$ and $G^z = G^{\text{fork}}$. For an operation $z$ in the trace $\tau$, $(T^\tau, G^\tau)$ is chosen as given above because of the following intuitive reasons. If $z$ is of type $\text{join}, \text{trigger}$ or $\text{deq}$, then the vector clock and incoming edges of $\text{task}(z)$ is updated through $\text{VC-JOIN}$ and edge propagation respectively when EventTrack analyzes $z$, which intuitively means that some HB information is passed to $z$ only after $z$ is evaluated. In case of other types of operations, all the required HB information is received by the executing task at state $(T^\tau, G^\tau)$ itself. If $z$ is of type $\text{fork}$, $\text{notify}$, post or enable which pass on HB information to other operations (see Table 4 and 5 in [2]) but do not receive any additional HB information through VC-JOIN or edge propagation after processing $z$, we define $(T^\tau, G^\tau)$ as $(T^z, G^z)$.

We define a notion of well-formedness of a state as follows.

Definition 1.1. An analysis state $\sigma = (T, G)$, with $G = (H, \xi)$, reached on processing a prefix $\alpha$ of a program trace $\tau$ is said to be well-formed if:

1. For an event $e \in Ev$ if $\text{deq}(e) \in \alpha$ and either $\text{end}(e) \notin \alpha$ or $\text{end}(e)$ is the last operation in $\alpha$, then for every task $b \in H$ such that $b \neq e$, $T(b)[C(e)] < T(e)[C(e)]$.
2. For a thread $t \in Th$ if $\text{tinit}(t) \in \alpha$ and either $\text{texit}(t) \notin \alpha$ or $\text{texit}(e)$ is the last operation in $\alpha$, then for every task $b \in H$ such that $b \neq t$, $T(b)[C(t)] < T(t)[C(t)]$.
3. For an event $e \in Ev$ if $\text{post}(e) \notin \alpha$ then for every task $b \in H$, $(e \xrightarrow{1} b) \notin G(b)$ for any value of edge label $l$.
4. For an event $e \in Ev$ if $\text{deq}(e) \notin \alpha$ then for every task $b \in H$, $(e \xrightarrow{1} b) \notin G(b)$.
5. For an event $e \in Ev$ if $\text{end}(e) \in \alpha$ then $T(e) = T^{\text{end}(e)}(e)$.
6. For a thread $t \in Th$ if $\text{texit}(e) \in \alpha$ then $T(t) = T^{\text{texit}(t)}(t)$.

Lemma 1.2. An analysis state $\sigma = (T, G)$ reached on processing a prefix $\alpha$ of a program trace $\tau$ is well-formed.

Proof. Let $\sigma^\tau = (T^\tau, G^\tau)$ be the initial state of EventTrack. Then for every task $b \in H$, $T^\tau(b) = \bot_H$ and $G^\tau(b) = \emptyset$. Then, trivially $\sigma^\tau$ is well-formed since $\alpha$ is an empty sequence. With this we can do a case by case analysis of each transfer function of EventTrack and using induction prove that any state $\sigma$ reached from $\sigma^\tau$ on processing a prefix $\alpha$ of $\tau$ is well-formed.

Lemma 1.3. In a trace $\tau$ of a program $P$ for an operation $z \in \tau$ and $b$ being $\text{task}(z)$,
(1) if an operation \( x \in \tau \) is such that \( T^x(\text{task}(x))[C(\text{task}(x))] \leq T^x(b)[C(\text{task}(x))] \) then \( x \prec_{hb} z \).

(2) for an event \( e \in Ev \) if \( (e \xrightarrow{0} b) \in \mathcal{G}^x(b) \) then \( \text{post}(e) \prec_{hb} z \).

(3) for an event \( e \in Ev \) if \( (e \xrightarrow{1} b) \in \mathcal{G}^x(b) \) then \( \text{deq}(e) \prec_{hb} z \).

\[ \text{Proof.} \text{ We prove this by induction on the number of steps of application of transfer functions required for EventTrack to derive antecedent of property (1), (2) or (3) of the lemma.} \]

We first prove property (1) of the lemma. Let \( d = \text{task}(x) \) and \( b = \text{task}(z) \). If \( d = b \) or \( C(d) = C(b) \) then by program order and total order property of chains, \( x \prec_{hb} z \). Assume \( d \neq b \) and \( C(d) \neq C(b) \). From Lemma 1.2 by which analysis state \( (T^x, \mathcal{G}^x) \) is well-formed, and the antecedent of property (1), we have \( T^x(b)[C(d)] < T^x(d)[C(d)] \leq T^x(b)[C(d)] \). Then, there exists an operation \( y \) executed between \( x \) and \( z \) in which passes \( T^x(d)[C(d)] \) to vector clock of \( b \) resulting in the following.

\[ T^x(b)[C(d)] \leq T^y(b)[C(d)] < T^x(d)[C(d)] \leq T^y(b)[C(d)] \leq T^z(b)[C(d)] \]  

**Base case.** In the base case, either \( x = y \) (e.g., \( x = \text{fork}(d, b) \)) or \( y = z \) (e.g., \( z = \text{join}(b, d) \)) and the happens-before from \( x \) to \( z \) can be established by single application of a non-event-ordering HB rule.

**Induction Hypothesis.** For a pair of operations \( o_1 \) and \( o_2 \) in the trace \( \tau \), if the relation \( T^{o_1}((\text{task}(o_1))[C(\text{task}(o_1))] \leq T^{o_1}((\text{task}(o_2))[C(\text{task}(o_1))] \) is derived within \( \eta \) steps of application of transfer functions of EventTrack then \( o_1 <_{hb} o_2 \). For an event \( e \in Ev \), an operation \( o \) and a task \( a = \text{task}(o) \), if \( (e \xrightarrow{1} a) \) can be established to be a member of \( \mathcal{G}^o(a) \) within \( \eta \) steps of application of transfer functions of EventTrack then \( \text{post}(e) <_{hb} o \). For an event \( e \in Ev \), an operation \( o \) and a task \( a = \text{task}(o) \), if \( (e \xrightarrow{1} a) \) can be established to be a member of \( \mathcal{G}^o(a) \) within \( \eta \) steps of application of transfer functions of EventTrack then \( \text{deq}(e) <_{hb} o \).

**Induction step.** Let \( T^x(d)[C(d)] \leq T^z(b)[C(d)] \) be established in \( \eta + 1 \) applications of transfer functions. From [1], the transfer function of \( y \) modifies the vector clock of \( b \). Then from the transfer functions given in Table 4 and 5 in [2], \( b = \text{target}(y) \). Let \( a = \text{source}(y) \). Then \( y \) is one of the operations among \( \text{fork}(a, b), \text{join}(b, a), \text{notify}(o) \) with \( b \) being the target, \( \text{trigger}(b), \text{post}(b), \) or \( \text{deq}(b) \). We prove the induction step for the case when \( y = \text{deq}(b) \) which involves reasoning about event-ordering rules. The other cases can be proved similarly using an applicable non event-ordering rule. Further, we prove the case when \( b \) is posted with a delay. The case when \( b \) is posted to the front of the queue can be proved using similar arguments. Let \( A \) be the set of events identified by the transfer function of \( \text{deq} \) in Table 4 in [2], and thus whose vector clock values and incoming edges will be used to update the vector clock and incoming edges respectively of the task \( b \). Due to [1], set \( A \) must contain at least one event whose vector clock was used to update \( T^y(b)[C(d)] \). Let \( e \in A \) be the event whose vector clock \( \text{VC-JOIN} \) was performed to result in \( T^x(d)[C(d)] \leq T^\text{deq}(b)(b)[C(d)] \). Projecting VC-JOIN between \( T^y(b) \) and \( T^y(e) \) to component \( C(d) \) we have,

\[ T^y(b)[C(d)] = \max(T^y(b)[C(d)], T^y(e)[C(d)]) \]  

\[ T^y(b)[C(d)] = T^y(e)[C(d)] \]  

from I & II [111]

Since event \( e \in A \), \( (e \xrightarrow{1} b) \in \mathcal{G}^y(b) \), for \( l \in \{0, 1\} \). Assume \( l = 1 \).

Note that case when \( l = 0 \) can be proved similarly. Then, the edge \( (e \xrightarrow{1} b) \) has been added to event graph in \( k \leq \eta \) steps. Therefore by induction hypothesis, \( \text{deq}(e) <_{hb} \text{deq}(b) \). Then, by NO-PRE rule in Table 1 in [2], \( \text{deq}(e) <_{hb} \text{deq}(b) \). This also indicates that EventTrack has processed \( \text{end}(e) \) prior to \( y \). Since analysis state \( \sigma^y \) is well-formed and from Definition 1.1 we have \( T^y(e) = T^{\text{end}}(e) \). Then, from [1] and [111], \( T^x(d)[C(d)] \leq T^\text{end}(e)[C(d)] \) and clearly this has been established within \( \eta \) steps. Therefore, \( x <_{hb} \text{end}(e) \) by induction hypothesis [1V]

Since \( y = \text{deq}(b), \text{task}(y) = b \). Then, \( y \) and \( z \) are HB ordered by program order. Combining 1V, NO-PRE HB rule and program order we have thus proved that \( x <_{hb} z \).

We can prove property (2) and (3) of the lemma by referring to properties (3) and (4) of the well-formedness definition and using analogous arguments as above.

The following lemma (Lemma 1.4) states the properties of vector clocks and event graph observed when a pair of operations get HB ordered. The operator \( \omega \) defined for event graph (see Table 2 in [2]) prunes or does not propagate certain edges if the information conveyed by those edges are already subsumed by vector clocks. Hence, the lemma also specifies the condition when an otherwise expected edge is not present in the event graph.

**Lemma 1.4.** Consider a trace \( \tau \) of a program \( P \) containing operations \( x, z \in \tau \) with \( b = \text{task}(z) \) such that \( x <_{hb} z \). Then,

1. \( T^x(\text{task}(x)) \subseteq T^z(\text{task}(z)) \).

2. **Additionally** if \( x = \text{post}(e) \) for an event \( e \in Ev \) then \( e = b \), or \( (e \xrightarrow{1} b) \in \mathcal{G}^z(b) \) for \( l \in \{0, 1\} \), or \( T^{\text{end}}(e)[C(d)] \subseteq T^z(b) \).

3. **Additionally** if \( x = \text{deq}(e) \) for an event \( e \in Ev \) then \( e = b \), or \( (e \xrightarrow{1} b) \in \mathcal{G}^z(b) \), or \( T^{\text{end}}(e)[C(d)] \subseteq T^z(b) \).

\[ \text{Proof.} \text{ We prove this by induction on the length of derivation of happens before ordering from } x \text{ to } z \text{ in } \tau. \text{ If there are multiple ways to derive } x <_{hb} z \text{ then induction is on the derivation which applies minimum number of happens-before rules. We prove this for the case where } x = \text{post}(e) \text{ such that } e \text{ is not posted to the front of an event queue (i.e., } \text{!fog}(e)) \text{. Proof for the rest of the types of } x \text{ can be given similarly. Let } b = \text{task}(z). \]

**Base case** \((k = 1)\). Let \( d = \text{task}(x) \). Operation \( x = \text{post}(e) \) is established to happen before \( z \) in one step only if \( x = \text{deq}(e) \) or \( z \) is the immediate next operation in \( x \)’s task \( d \). In the former case \( \text{task}(z) = b = e \). In the latter case where \( d = b \), on processing \( \text{post}(e) \) the transfer function of \( \text{post} \) adds an edge \( (e \xrightarrow{0} d) \). Also, in both the cases the application of transfer function of post results in \( T^z(d) \subseteq T^z(b) \). Hence proved.

**Induction Hypothesis.** If the happens-before ordering from an operation \( o_1 \) to \( o_2 \) can be established within \( \eta \) steps of application of HB rules, where \( h_1 = \text{task}(o_1) \) and \( h_2 = \text{task}(o_2) \), then \( T^{o_1}(h_1) \)
\[ \exists T^0(h_2). \] In addition if \( o_1 = \text{post}(e) \) then \( e = h_2, \) or \( (e \xrightarrow{0/1} h_2) \in G^0(h_2), \) or \( T^{\text{ende}}(e) \subseteq T^0(h_2). \) Instead if \( o_1 = \text{deq}(e) \) then \( e = h_2, \) or \( (e \xrightarrow{1/0} h_2) \in G^0(h_2), \) or \( T^{\text{ende}}(e) \subseteq T^0(h_2). \)

**Induction step** \((k = n + 1).\) There exists an intermediate operation \( y \) such that \( \text{post}(e) \preceq_{hb} y \) in \( k_1 \) steps and \( y \preceq_{hb} z \) in \( k_2 \) steps such that \( k_1 + k_2 = n + 1. \) We choose \( y \) such that \( y \preceq_{hb} z \) is established by the application of a HB rule other than transitivity, i.e., any event-ordering rule, program order or a base HB rule such as notify happens before wait and so on. Such an intermediate operation exists because even if \( y \preceq_{hb} z \) due to transitivity, then by the antecedent of transitive HB rule there must be an intermediate \( y' \) such that \( y \preceq_{hb} y' \) and \( y' \preceq_{hb} z. \)

Let \( d = \text{task}(y) \) and \( b = \text{task}(z). \) Since \( k_1 + k_2 = n + 1, \) we have \( k_1 \leq n. \) Then by induction hypothesis,

\[ T^x(\text{task}(x)) \subseteq T^y(d), \text{ where } x = \text{post}(e) \tag{1} \]
\[ d = e, \text{ or } (e \xrightarrow{0/1} d) \in G^y(d), \text{ or } T^{\text{ende}}(e) \subseteq T^y(d) \tag{11} \]

If \( b = e \) then we can trivially prove the relevant properties of the lemma for the induction step. Assume \( b \neq e \) henceforth. We prove the induction step either by appealing to the action of the transfer function corresponding to the HB rule that establishes \( y \preceq_{hb} z, \) or by establishing the existence of a HB derivation which takes less than \( k_2 \) steps to derive \( y \preceq_{hb} z \) and consequently less than or equal to \( n \) steps to derive \( x \preceq_{hb} z. \)

We first consider the case where \( y \preceq_{hb} z \) due to some HB rule other than an event-ordering rule. Then from the corresponding transfer function in Table 4 or Table 5 (see [2]), we can establish the following. The vector clock updates performed by any of the transfer functions establishing \( y \preceq_{hb} z \) results in \( T^y(d) \subseteq T^y(b). \) Combining this with [1] we can derive \( T^x(\text{task}(x)) \subseteq T^x(b), \) thus proving property (1) of the lemma. We will now prove property (2) of the lemma. If \( d = e \) is the disjunct satisfied in [11], then the transfer function adds an edge \( (e \xrightarrow{0/1} a) \). Assume \( d \neq e \) and \( T^{\text{ende}}(e) \subseteq T^y(d) \) to be the disjunct satisfied in [11]. Then the transfer function establishing \( y \preceq_{hb} z \) computes \( T^y(d) \) by performing VC-JOIN of vector clock of \( b \) with \( T^y(d), \) either by consulting the lookup table or directly with the current vector clock value of \( d. \) Hence, \( T^y(d) \subseteq T^y(b). \) Combining this with the assumption made we thus have \( T^{\text{ende}}(e) \subseteq T^y(b). \) If the above two cases do not hold, the remaining case in [11] is that \( (e \xrightarrow{0/1} d) \in G^y(d). \) If \( d = b \) then it is trivial. If not, the transfer function establishing the ordering between \( y \) and \( z \) propagates \( G^y(d) \) to \( b \) resulting in an edge \( (e \xrightarrow{0/1} b). \) From the definition of \( \psi \) given in Table 2 in [2], this edge is not added only if \( T^{\text{ende}}(e) \subseteq T^y(b). \) We have thus proved property 2 of the lemma for the induction step when \( y \preceq_{hb} z \) is established by a non event-ordering HB rule and \( x \preceq_{hb} y \) causes any disjunct in II to be satisfied.

We now consider the case where \( y \preceq_{hb} z \) due to an event-ordering rule. We provide a proof for the FIFO rule and the rest can be proved similarly. Then \( d \) and \( b \) are events on the same thread such that \( y = \text{end}(d) \) and \( z = \text{deq}(b). \) Since \( y \) and \( z \) are ordered by \( \text{FIFO}(a) \) or \( \text{FIFO}(b) \) rule we have \( \text{post}(d) \preceq_{hb} \text{post}(b). \) As assumed for the induction step, the ordering from \( y \) to \( z \) is established in \( k_2 \leq n \) steps. This implies that,

The ordering from \( \text{post}(d) \) to \( \text{post}(b) \) is established

in at most \( k_2 - 1 \) steps, where \( k_2 \leq n \) \[ [11] \]

Let \( a = \text{task}(\text{post}(b)). \) From \([111], \) induction hypothesis is applicable to the derivation of \( \text{post}(d) \preceq_{hb} \text{post}(b). \) Then, the following property holds.

\[ a = d \quad \text{or} \quad (d \xrightarrow{0/1} a) \in \mathcal{G}^{\text{post}}(b)(a) \]
\[ \text{or } T^{\text{ende}}(d) \subseteq T^{\text{post}}(b)(a) \tag{11} \]

If either of the disjuncts \( a = d \) or \((d \xrightarrow{0/1} a) \in \mathcal{G}^{\text{post}}(b)(a) \) in [11] holds, then the transfer function of \( \text{post} \) adds an edge \((d \xrightarrow{0/1} b) \) or \((d \xrightarrow{1/0} b) \) respectively. For both these cases on processing \( z = \text{deq}(b) \) the transfer function of \( \text{deq} \) adds \( d \to \) to set \( A \) (see transfer function of \( \text{deq} \) in Table 4 in [2]) which results in the propagation of all incoming edges of \( d \to b \) using \( \psi \) and computation of \( T^y(b) \) such that \( T^y(d) \subseteq T^y(b). \) Then, we can make arguments similar to those made when \( y \) and \( z \) were assumed to be ordered by a non event-ordering rule and thus prove the induction statement for properties 1 and 2 of the lemma.

Consider the case where the disjunct \( \text{post}(d) \) satisfies \( \mathcal{G}^{\text{post}}(b)(a) \) of [11] holds. Note that from our definition, for any post operation \( o \) we have \( \mathcal{G}^o = \mathcal{G}^0. \) Then from the transfer function of \( \text{post} \) we get \( T^{\text{ende}}(d) \subseteq T^{\text{post}}(b)(a) \) which with at most \( k_2 - 1 \) steps of application of HB rules (from \([111] \) and \([11v], \)) we derived \( T^y(d) \subseteq T^y(b) \) and consequently \( y \preceq_{hb} z \) purely by using transfer functions and Lemma 1.3. Therefore, \( y \preceq_{hb} z \) is established in at most \( k_2 - 1 \) steps which in turn means \( \text{post}(e) \preceq_{hb} z \) in at most \( n \) steps. Then properties 1 and 2 of the Lemma hold by induction hypothesis. \( \square \)

**Theorem 1.5.** In a trace \( \tau \) of a program \( P \) evaluated by EventTrack, for a pair of operations \( x, z \in \tau \) such that \( d = \text{task}(x) \) and \( b = \text{task}(z), \)

1. \( x \preceq_{hb} y \quad \text{iff} \quad T^x(d) \subseteq T^z(b). \)
2. \( x = \text{post}(e) \text{ for an event } e \in Ev \text{ then, } x \preceq_{hb} z \quad \text{iff} \quad e = b, \)
   \( \text{or } (e \xrightarrow{0/1} b) \in \mathcal{G}^{z}(b) \text{ for } l \in \{0,1\}, \) or \( T^{\text{ende}}(e) \subseteq T^z(b). \)
3. \( x = \text{deq}(e) \text{ for an event } e \in Ev \text{ then, } x \preceq_{hb} z \quad \text{iff} \quad e = b, \)
   \( \text{or } (e \xrightarrow{1/0} b) \in \mathcal{G}^{z}(b), \text{ or } T^{\text{ende}}(e) \subseteq T^z(b). \)

**Proof.** From Lemma 1.3 and Lemma 1.4. \( \square \)

2 **Complexity Analysis**

We present a detailed complexity analysis for EventTrack and compare it with that of EventRacer. Computations performed on vector clocks and event graph determine the time complexity of EventTrack. Whereas for EventRacer, computations performed on vector clocks and HB graph determine this. Both these HB computation techniques for event-driven programs have the same time complexity to perform vector clock updates. Hence, we present the complexity of EventTrack only in terms of computing candidate events using event graph and compare it with that of computing candidate events using HB graph.
Table 1: Complexity of event graph operations with a bound $k$ on the number of incoming edges from tasks on each thread.

<table>
<thead>
<tr>
<th>Operation</th>
<th>Complexity</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cost of edge retrieval for source events from a given thread</td>
<td>$O(k)$</td>
</tr>
<tr>
<td>Cost of inserting an edge into a group of edges with source events from the same thread</td>
<td>$O(k)$</td>
</tr>
<tr>
<td>Cost of edge propagation with pruning</td>
<td>$O(k +</td>
</tr>
</tbody>
</table>

**Analysis of event graph of EventTrack.** The incoming edges of all the nodes in an event graph $G = (H, E)$ are maintained in a sorted manner as mentioned in Section 4.3 in [2]. The main operations performed on an event graph $G$ are (1) inspecting incoming edges of a task $b$ by performing edge retrieval $G(b)$, (2) inserting an edge into $G$ using $\odot$ or $\otimes$, and (3) propagating incoming edges of a task $d$ to task $b$ along with pruning incoming edges of $b$ using operator $\oplus$. Clearly, the cost of retrieving edges of $b$ is $O(|G(b)|)$. The incoming edges of every node in $G$ are stored as a linked list which is always maintained sorted as per the $\Sigma$ IDs of source events. Hence, the cost of inserting an edge into a node $b$ is $O(|G(b)|)$. Propagating edges with pruning involves inspection of all the incoming edges of the source task $d$ as well as source task $b$, making its complexity $O(|G(b)| + |G(d)|)$.

If we exclude edge retrieval performed internally by edge propagation with pruning (using operator $\odot$), we observe that EventTrack performs edge retrieval on $G$ only when evaluating an operation posting an event $e$ to the front of a queue, and when identifying candidate events of an event $e$ when evaluating deq(e). In both of these cases it is sufficient to only inspect a subset of $G(e)$ with source events belonging to dest(e). Hence, the incoming edges of every node are grouped into threads corresponding to their source tasks and are sorted using $\Sigma$ IDs only within these thread groups. The edges in the event graph are transitively propagated thus making the event graph generation cost $O(|H|^2)$ in the worst case, where $H$ is the set of all tasks in the given trace. However, the operator $\oplus$ used to transitively propagate edges also pruning edges, thus preventing further propagation of pruned edges. In the presence of such a pruning let us assume that every task has at most $k$ incoming edges from tasks belonging to each thread. Then, the maximum in-degree of any node in event graph is $in_{max} = k + |Th|$. With this bound on the number of incoming edges from each thread and thread based grouping of incoming edges, the revised complexities of operations are given in Table 1.

The transfer function of each of the operations except deq inserts at most two edges with a cost of $O(k)$, and performs an edge propagation with cost $O(k + |Th|)$. EventTrack orders each event directly only with a corresponding event-covering set. Let the size of event-covering set of each event be at most $K$. Then, including the cost of inspecting incoming edges to obtain the set of candidate events, the complexity of event graph computations for deq’s transfer function is $O(k) + O(k + |Th| + K)$. Hence, the complexity of performing event graph computations on all the operations so as to aid in identifying sets of candidate events is:

$$O(|Op| * (k + k * |Th|)) + O(|Ev| * k * |Th| + K)$$

If we assume the number of events to be linearly proportional to the number of operations and retain only dominant components of the complexity computed, we can derive the complexity of event graph computations as $O(|Op| * k * |Th|) = O(|Op| * \text{in}_{max} * K)$.

**Analysis of HB graph of EventRacer.** EventRacer performs DFS traversals on HB graph over operations in the given trace, to identify the set of candidate events for each event. Let the number of such DFS traversals performed by EventRacer be at most $d$ per event in $Ev$. Then, the complexity of computing candidate events using HB graph and identifying HB ordered events for all the events is $O(d * |Op| * |Ev|)$.

From our complexity analysis, complexity of computing sets of candidate events for EventTrack is linear in the number of operations in the trace, linear in the size of event-covering sets and linear in the maximum in-degree of nodes in event graph. Whereas for EventRacer, the complexity of computing sets of candidate events is linear in the number of operations, linear in the number of events and linear in the number of DFS traversals performed per event. These two complexities are incomparable. This is because, the actual values of $in_{max}$ and $K$ which feature in the complexity computed for EventTrack and the value of $d$ which features in the complexity computed for EventRacer, are an artifact of the HB relation, and hence hard to compare analytically. Thus, we have performed an empirical evaluation and comparison of these two HB computation techniques.

**REFERENCES**
